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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) KIM, Sang-Soo; KIM, Dong-Gyu
Assignee: SAMSUNG ELECTRONICS CO., LTD.
Title: THIN FILM TRANSISTOR ARRAY PANEL
Application No.: 10/522,848 Filing Date: 09/20/2005
Examiner: WEISS, Howard Group Art Unit: 2814
Docket No.: AB-1410 US Confirmation No.: 9371

San Jose, California
November 15, 2007

Mail Stop Amendment
COMMISSIONER FOR PATENTS
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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(b)**

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying Form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed with the exception of any U.S. Patents and U.S. Patent Publications. The documents were cited by the Korean Patent Office in the International Search Report for International Application No. PCT/KR2002/001763, which is the international stage of the present application.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be material to patentability as defined in § 1.56(b).

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No fee is believed to be required. If a fee is required for this Information Disclosure Statement, please charge the fee to Deposit Account No. 50-2257. This paper is being submitted in duplicate.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 15, 2007.


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Nov. 15, 2007
Date of Signature

Respectfully submitted,



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